

## LIST OF EXPERIMENTS-ME

SL NO	NAME OF LAB/ WORKSHOP	LIST OF EXPERIMENTS
1	MECHANICAL ENGINEERING WORKSHOP	Study of mechanical tools, components and their applications
		Exercises on Carpentry
		Exercises on Smithy
		Exercises on Foundry
		Exercises on Sheet Metal Forming
		Exercises on Arc Welding
		Study of various quenching medium
		Exercises on Filing
		Demonstration and applications of Drilling Machine, Grinding Machine, Shaping Machine, Milling machine and Lathe
2	COMPUTER AIDED MACHINE DRAWING	Sketching of conventional representation of Riveted joints(Manual Drawing)
		Sketching of conventional representation of welded joints(Manual Drawing)
		Sketching of conventional representation of Bolts and Nuts (Manual Drawing)
		Sketching of conventional representation of Foundation Bolts(Manual Drawing)
		Preparation of production drawing of Cotter joint(Manual Drawing)
		Preparation of production drawing of Knuckle joint(Manual Drawing)
		Preparation of production drawing of Flange Coupling(CAD Drawing)
		Preparation of production drawing of Flexible Coupling(CAD Drawing)
		Preparation of production drawing of Oldham's Coupling(CAD Drawing)
		Preparation of assembly drawing of Universal Joint(CAD Drawing)
Preparation of assembling drawing of Plummer Block(CAD Drawing)		
Preparation of assembly drawing of Ram's Bottom Safety Valve(CAD Drawing)		
3	FM & HM LAB	Determination of coefficient of discharge and calibration of Notches.
		Determination of coefficient of discharge and calibration of Orifice meter.
		Determination of coefficient of discharge and calibration of Venturi meter.
		Determination of hydraulic coefficients of orifices.
		Determination of Chezy's constant and Darcy's coefficient on pipe friction apparatus.
		Experiments on hydraulic ram.
		Reynolds experiment
		Bernoulli's experiment.
		Determination of metacentric height and radius of gyration of floating bodies
		Performance test on positive displacement pumps.
		Performance test on centrifugal pumps, determination of operating point and efficiency
		Performance test on gear pump.
Performance test on Impulse turbines.		
Performance test on reaction turbines (Francis and Kaplan Turbines).		
Speed variation test on Impulse turbine.		
4	MACHINE TOOLS LAB-I	STUDY OF LATHE TOOLS
		EXERCISES ON CENTRE LATHE(FACING, TURNING, THREAD CUTTING etc.)
		EXERCISES ON DRILLING MACHINE (DRILLING, BORING, REAMING, TAPPING ETC)
		EXERCISES ON SHAPING MACHINE (PREPARATION OF FLAT SURFACE, GROOVES, SLOTS ETC)
		EXERCISES ON SLOTTING MACHINE(PREPARATION OF FLAT SURFACE, GROOVES, SLOTS ETC)
		EXERCISES ON GEAR CUTTING OPERATION
		EXERCISES ON CYLINDRICAL GRINDING
		EXERCISES ON TOOL GRINDING
		MEASUREMENT OF CUTTING FORCES IN TURNING PROCESS
		EFFECT OF QUENCHING MEDIUM ON HARDNESS OF THE MATERIALS
		MEASUREMENT OF CUTTING TEMPERATURE IN TURNING PROCESS
		MICROSTRUCTURE PREPARATION OF VARIOUS METALS
		MEASUREMENT OF ANGLE USING SINE BAR
5	MACHINE TOOLS LAB-II	DETERMINATION OF SPEED OF A ROTATING BODY USING STROBOSCOPE
		CALIBRATION OF LOAD CELL
		CALIBRATION OF MICROMETER
		CALIBRATION OF VERNIER CALIPER
		VIBRATION MEASUREMENT
		CNC PROGRAMMING AND EXPERIMENT
		MEASUREMENT OF STRAIN USING STRAIN GAUGE
		MEASUREMENT OF SCREW THREAD PARAMETER USING PROFILE PROJECTOR
		MEASUREMENT OF SCREW THREAD PARAMETER USING TOOL MAKERS MICROSCOPE
		DETERMINE ANGLES OF SINGLE POINT CUTTING TOOL USING TOOL MAKERS MICROSCOPE
		CALIBRATION OF LVDT
		MEASUREMENT OF GEAR PARAMETERS USING PROFILE PROJECTOR
		Conduct load test on four stroke Diesel Multi cylinder engine with rope brake dynamometer.
Conduct load test on four stroke Diesel single cylinder engine with rope brake dynamometer.		
Conduct load test on four stroke Diesel single cylinder engine with eddy current dynamometer.		
Conduct volumetric efficiency test on four stroke diesel single cylinder engine.		
Conduct load test on four stroke petrol single cylinder engine with electrical loading dynamometer.		

6	THERMAL ENGINEERING LAB- I	<p>Conduct test using Pensky Marten closed cup flash point apparatus to determine the flash and fire point of the given oil.</p> <p>Conduct test to determine the viscosity of given oil using Redwood Viscometer.</p> <p>Conduct cooling curve test on a four stroke diesel twin cylinder engine</p> <p>Conduct Morse's test on four stroke petrol multi cylinder engine.</p> <p>Conduct heat balance test.</p> <p>Morse Test on Multi cylinder Petrol Engine</p> <p>Valve Timing Diagram for Single Cylinder Diesel Engine</p> <p>Retardation Test on Multi-Cylinder Diesel Engine</p>
7	COMPUTER AIDED DESIGN & ANALYSIS LAB	<p>Creating 3D part model of Bolts and Nuts</p> <p>Creating 3D part model of Flange Coupling</p> <p>Creating 3D assembly model of Socket and spigot joint</p> <p>Creating 3D assembly model of Knuckle Joint</p> <p>Creating 3D assembly models of Plummer block</p> <p>Creating 3D assembly model of Screw jack</p> <p>Static analysis on Simply Supported Beam with a point load</p> <p>Stress analysis of Cantilever Beam with a point load</p> <p>Static analysis on Simply Supported Beam with Uniformly distributed load</p> <p>Static analysis on a rectangular plate with a circular hole</p> <p>Conductive heat transfer analysis on 2-D plate</p> <p>Conductive and convective heat transfer analysis in furnace wall</p> <p>Laminar Flow Analyses in a 2-D Duct</p>
8	THERMAL ENGINEERING LAB- II	<p>Determination of LMTD and effectiveness of parallel flow, counter flow and cross flow heat exchangers</p> <p>Measurement of unsteady state conduction heat transfer</p> <p>Determination of heat transfer coefficients in free convection</p> <p>Determination of heat transfer coefficients in forced convection</p> <p>Determination of thermal conductivity of solids (composite wall)</p> <p>Determination of emissivity of a specimen</p> <p>Determination of Stefan Boltzman Constant</p> <p>Study and performance test on Refrigeration equipment(Refrigeration test rig)</p> <p>Study and performance test on Air conditioning equipment(Air conditioning test rig)</p> <p>Performance study on Heat pipe</p> <p>Calibration of Thermocouples</p> <p>Calibration of Pressure Guage</p>
9	MECHANICAL ENGINEERING LAB	<p>Study and performance test on Refrigeration equipment(Refrigeration test rig)</p> <p>Study and performance test on Air conditioning equipment(Air conditioning test rig)</p> <p>Performance study on Heat pipe</p> <p>Determination of efficiency of a blower</p> <p>Determination of efficiency of an air compressor</p> <p>Determination of natural frequency of free vibrations(Free vibration apparatus)</p> <p>Determination of natural frequency of free vibrations(Forced vibration apparatus)</p> <p>Determination of critical speed of shafts</p> <p>Experimental verification of Dunkerley's principle</p> <p>Experiment to demonstrate static balancing and dynamic balancing</p> <p>Experiment to determine the Gyroscopic couple and it's effects</p> <p>Determination of performance characteristics of a spring loaded Governor( Universal Governor apparatus)</p> <p>Preparation and Metallographic examination of a material specimen</p>
10	COMPUTER INTEGRATED MANUFACTURING LAB-1	<p>Exercises on 2D Modelling</p> <p>Exercises on 3D Solid Modeling</p> <p>Exercises on Assembly of 3D Models.</p> <p>Exercises on Simulation of 3D Models</p> <p>Exercises on Structural Analysis</p> <p>Exercises on ANOVA</p>
11	COMPUTER INTEGRATED MANUFACTURING LAB-2	<p>Excercise on CAM Express Software</p> <p>Automation using Hydraulics</p> <p>Automation using pneumatics</p> <p>PLC Programming and Implementation</p> <p>Exercises on CNC programming</p>

## LIST OF EXPERIMENTS-CE

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1	CIVIL WORKSHOP	<p>1. CALCULATE THE AREA OF A BUILT-UP SPACE AND A SMALL PARCEL OF LAND- USE STANDARD MEASURING TAPE AND DIGITAL DISTANCE MEASURING DEVICES EXERCISE</p> <p>2. (A) USE SCREW GAUGE AND VERNIER CALLIPER TO MEASURE THE DIAMETER OF A STEEL ROD AND THICKNESS OF A FLAT BAR (B) TRANSFER THE LEVEL FROM ONE POINT TO ANOTHER USING A WATER LEVEL (C) SET OUT A ONE ROOM BUILDING WITH A GIVEN PLAN AND MEASURING TAPE EXERCISE</p> <p>3. FIND THE LEVEL DIFFERENCE BETWEEN ANY TWO POINTS USING DUMPY LEVEL EXERCISE</p> <p>4. (A) CONSTRUCT A 1 1/2 THICK BRICK WALL OF 50 CM HEIGHT AND 60 CM LENGTH USING ENGLISH BOND. USE SPIRIT LEVEL TO ASSESS THE TILT OF WALLS. (B) ESTIMATE THE NUMBER OF DIFFERENT TYPES OF BUILDING BLOCKS TO CONSTRUCT THIS WALL.</p> <p>5. (A) INTRODUCE THE STUDENTS TO PLUMBING TOOLS, DIFFERENT TYPES OF PIPES, TYPE OF CONNECTIONS, TRAPS, VALVES, FIXTURES AND SANITARY FITTINGS. (B) DEMONSTRATED A SMALL RAINWATER HARVESTING INSTALLATION IN THE CAMPUS</p>
2	SURVEY LAB	<p>1. INTRODUCTION TO CONVENTIONAL SURVEYING A. CHAIN SURVEYING B. COMPASS SURVEYING LEVELLING</p> <p>2. SIMPLE LEVELING</p> <p>3. DIFFERENTIAL LEVELLING</p> <p>4. FLY LEVELLING</p> <p>THEODOLITE SURVEYING</p> <p>5. DISTANCE BETWEEN ACCESSIBLE POINTS (HORIZONTAL ANGLE)</p> <p>6. DISTANCE BETWEEN INACCESSIBLE POINTS (HORIZONTAL ANGLE)</p> <p>7. LEVEL DIFFERENCE BETWEEN POINTS (VERTICAL ANGLE)</p> <p>8. TANGENTIAL TACHEOMETRY (VERTICAL ANGLE)</p> <p>9. HEIGHT OF BUILDING (VERTICAL ANGLE)</p> <p>TOTAL STATION SURVEY</p> <p>10. HEIGHTS AND DISTANCES</p> <p>11. AREA COMPUTATION</p> <p>12. STUDY OF INSTRUMENTS A. AUTOMATIC LEVEL B. DIGITAL LEVEL C. HANDHELD GPS</p>

3	MATERIAL TESTING LAB - I	<p>CONDUCTING UNIAXIAL TENSION TEST ON ROD SPECIMENS</p> <p>2. STUDY ON STRESS-STRAIN CHARACTERISTICS OF TOR STEEL BY CONDUCTING UNIAXIAL TENSION TEST ON ROD SPECIMENS</p> <p>3. STUDY ON ESTIMATION OF SHEAR CAPACITY OF MILD STEEL SPECIMEN BY CONDUCTING A DOUBLE SHEAR TEST ON ROD SPECIMEN.</p> <p>4. STUDY ON FLEXURAL BEHAVIOUR OF STEEL BY CONDUCTION OF TEST ON RSJ (I CROSS SECTION)</p> <p>5. STUDY ON TORSIONAL BEHAVIOUR AND ESTIMATION OF MODULUS OF RIGIDITY OF STEEL BY CONDUCTING TORSION TEST ON ROD SPECIMENS</p> <p>6. STUDY ON ESTIMATION OF MODULUS OF RIGIDITY OF STEEL AND BRASS / COPPER MATERIALS UTILIZING THE PRINCIPLES OF TORSIONAL VIBRATIONS.</p> <p>7. STUDY ON ESTIMATION OF TOUGHNESS PROPERTIES OF STEEL SPECIMENS BY CONDUCTING (A) IZOD &amp;(B) CHARPY IMPACT TESTS.</p> <p>8. STUDY ON ESTIMATION OF HARDNESS PROPERTIES OF ENGINEERING MATERIALS SUCH AS BRASS, ALUMINIUM, COPPER, STEEL ETC.BY PERFORMING BRINELL HARDNESS TEST</p> <p>9. STUDY ON ESTIMATION OF MODULUS OF RIGIDITY OF STEEL BY PERFORMING TENSION TESTS ON SPRING SPECIMENS.</p> <p>10. STUDY ON ESTIMATION OF MODULUS OF RIGIDITY OF STEEL BY PERFORMING COMPRESSION TESTS ON SPRING SPECIMENS</p> <p>11. STUDY ON FLEXURAL BEHAVIOUR OF TIMBER MATERIAL BY PERFORMING TESTS ON BEAM SPECIMENS.</p> <p>12. EXPERIMENT ON VERIFICATION OF MAXWELL'S RECIPROCAL</p>
4	MATERIAL TESTING LAB - II	<p>1. TESTING OF CEMENT: FINENESS, NORMAL CONSISTENCY,INITIAL &amp; FINAL SETTING TIME</p> <p>2. TESTING OF CEMENT: SPECIFIC GRAVITY AND COMPRESSIVE STRENGTH</p> <p>3. STUDY ON SOUNDNESS OF CEMENT</p> <p>4. TESTING OF COARSE AND FINE AGGREGATE: SIEVE ANALYSIS</p> <p>5. TESTING OF COARSE AND FINE AGGREGATE: WATER ABSORPTION, BULK DENSITY, VOID RATIO, POROSITY AND SPECIFIC GRAVITY</p> <p>6. TEST ON BULKING OF SAND</p> <p>7. TESTS ON FRESH CONCRETE :MEASUREMENT OF WORKABILITY OF CONCRETE BY SLUMP CONE TEST AND COMPACTING FACTOR TEST</p> <p>8. STUDY ON WORKABILITY OF CONCRETE BY VEE-BEE TEST AND FLOW TEST.</p> <p>9. CONCRETE MIX DESIGN BY IS CODE METHOD AND CASTING OF CUBES, CYLINDERS WITH DESIGNED CONCRETE MIXES.</p> <p>10. TESTS ON HARDENED PROPERTIES OF CONCRETE: COMPRESSIVE, SPLIT AND FLEXURAL STRENGTH.</p> <p>11. TESTS ON HARDENED PROPERTIES OF CONCRETE: MODULUS OF ELASTICITY OF CONCRETE</p> <p>12. TESTS ON BRICK, FLOOR AND ROOF TILES AS PER IS CODAL PROVISION.</p>

5	GEOTECHNICAL ENGINEERING LAB	<p>ESTIMATION OF PHYSICAL AND INDEX PROPERTIES OF THE GIVEN SOIL:  AFTER PERFORMING THE SET OF EXPERIMENTS, STUDENTS ARE EXPECTED TO INFER THE RESULTS OF THE EXPERIMENTS IN THEIR ENGINEERING BEHAVIOR.</p> <ol style="list-style-type: none"> <li>1. DETERMINATION OF WATER CONTENT AND SPECIFIC GRAVITY</li> <li>2. SIEVE ANALYSIS</li> <li>3. HYDROMETER/PIPETTE ANALYSIS</li> <li>4. ATTERBERG LIMITS (LIQUID LIMIT, PLASTIC LIMIT AND SHRINKAGE LIMIT)</li> <li>5. SWELLING TEST CIVIL ENGINEERING</li> <li>6. FIELD DENSITY DETERMINATION</li> </ol> <p>PART B</p> <p>DETERMINATION OF ENGINEERING PROPERTIES OF THE GIVEN SOIL:  STUDENTS SHOULD BE FAMILIARIZING WITH THE TESTS TO BE PERFORMED TO DETERMINE THE ENGINEERING PROPERTIES OF THE GIVEN SOIL AND INTERPRET THE RESULTS FOR FIELD APPLICATION.</p> <ol style="list-style-type: none"> <li>7. PERMEABILITY TEST</li> <li>8. STANDARD PROCTOR COMPACTION TEST</li> <li>9. HEAVY COMPACTION</li> <li>10. CALIFORNIA BEARING RATIO TEST</li> <li>11. DIRECT SHEAR TEST</li> <li>12. UNCONFINED COMPRESSION TEST</li> <li>13. CONSOLIDATION TEST</li> </ol> <p>STUDY/DEMONSTRATION</p> <ol style="list-style-type: none"> <li>14. TRIAXIAL TEST</li> </ol>
6	CIVIL ENGINEERING PLANNING AND DRAFTING LAB	<ol style="list-style-type: none"> <li>2. DRAW SECTIONAL DETAILS AND ELEVATION OF GLAZED WINDOWS AND VENTILATORS IN WOOD.</li> <li>3. DRAW SECTIONAL DETAILS , DETAILING ON FIXING ARRANGEMENT AND ELEVATION OF STEEL WINDOWS.</li> <li>4. DRAW ELEVATION, SECTION AND DETAILING OF CONNECTION BETWEEN MEMBERS, ARRANGEMENT FOR FIXING AT THE SUPPORT FOR STEEL ROOF TRUSS.</li> <li>5. DRAW PLAN, SECTION AND ELEVATION OF DOG LEGGED STAIRCASE.</li> <li>6. DRAW PLAN, SECTION AND ELEVATION OF SINGLE STORIED RESIDENTIAL BUILDINGS WITH FLAT ROOF.</li> <li>7. DRAW PLAN, SECTION AND ELEVATION OF TWO STORIED RESIDENTIAL BUILDING.</li> <li>8. DRAW PLAN , SECTION AND ELEVATION OF A COMMUNITY HALL HAVING CORRUGATED GI SHEET ROOF.</li> <li>9. PREPARE A SITE PLAN AND SERVICE PLAN AS PER LATEST BUILDING RULES (KPBR OR KMBR)</li> <li>10. PREPARE DETAILED DRAWING ON BUILDING SERVICES (FOR SINGLE AND TWO STORIED BUILDINGS ONLY) AND ON-SITE WASTEWATER DISPOSAL SYSTEMS LIKE SEPTIC TANK AND SOAK PIT. .</li> <li>11. DRAW PLAN, SECTION AND ELEVATION OF A PUBLIC BUILDINGS—OFFICE COMPLEX, PUBLIC HEALTH CENTRE, POST OFFICE, BANK ETC</li> <li>12. DRAW PLAN, SECTION AND ELEVATION OF A INDUSTRIAL BUILDING WITH CORRUGATED GI STEEL ROOF AND PEB BASED WALLING ELEMENTS.</li> </ol>

7	CIVIL ENGINEERING SOFTWAREWARE LAB	<p>STANDARD SOFTWARE USED IN THE INDUSTRY.</p> <p>EXERCISE 1: ANALYSIS AND DESIGN OF CONTINUOUS AND CANTILEVER BEAMS EXERCISE 2; ANALYSIS AND DESIGN OF PLANE TRUSS AND FRAMES</p> <p>EXERCISE 3:ANALYSIS AND DESIGN OF MULTI-STORIED RCC FRAMED STRUCTURES.</p> <p>2. PREPARATION OF STRUCTURAL DRAWINGS OF SLABS AND BEAMS</p> <p>EXERCISE 4:DETAILED STRUCTURAL DRAWING OF ONE WAY / TWO WAY AND CONTINUOUS SLABS.</p> <p>EXERCISE 5: DETAILED STRUCTURAL DRAWING OF SINGLY REINFORCED / DOUBLE REINFORCED BEAMS.</p> <p>EXERCISE 6: DETAILED STRUCTURAL DRAWING OF CONTINUOUS / FLANGED BEAMS. EXERCISE 7: DETAILED STRUCTURAL DRAWING OF FOUNDATION UNITS – ISOLATED AND COMBINED FOOTING (RECTANGULAR)</p> <p>3. USE OF BUILDING INFORMATION MODELLING TOOLS INTRODUCTION TO BIM PROCESS AND DESCRIBE THE WORKFLOW IN USING BIM IN THE BUILDING LIFECYCLE</p> <p>EXERCISE 8: PREPARATION OF BUILDING MODEL FROM A GIVEN ARCHITECTURAL DRAWING OF A RESIDENTIAL UNIT AND PERFORM MODEL BASED COST ESTIMATION EXERCISE 9:CREATE A SCHEDULE AND IMPORT IT INTO THE 4D MODELLING ENVIRONMENT, SO THAT EACH ACTIVITY IN THE SCHEDULE CAN BE LINKED TO AN OBJECT IN THE MODEL.</p> <p>EXERCISE 10:DEVELOP SCHEDULES FOR THE CONSTRUCTION OF SLABS, WALLS, COLUMNS, BEAMS AND WINDOWS OF A SECTION OF A</p>
8	TRANSPORTATION ENGINEERING LAB	<p>TEST ON SOIL</p> <ol style="list-style-type: none"> <li>1. CALIFORNIA BEARING RATIO TEST (SOAKED/UNSOAKED SPECIMEN)</li> <li>TEST ON COARSE AGGREGATE</li> <li>2. SPECIFIC GRAVITY AND WATER ABSORPTION TEST</li> <li>3. AGGREGATE IMPACT TEST</li> <li>4. LOS ANGELES ABRASION TEST</li> <li>5. AGGREGATE CRUSHING VALUE TEST</li> <li>6. SHAPE TEST (ANGULARITY NUMBER, FLAKINESS INDEX, ELONGATION INDEX, COMBINED FLAKINESS AND ELONGATION INDEX)</li> <li>7. STRIPPING VALUE OF ROAD AGGREGATES</li> </ol> <p>TESTS ON BITUMEN</p> <ol style="list-style-type: none"> <li>8. DETERMINATION OF GRADE OF BITUMEN BASED ON VISCOSITY</li> <li>9. SOFTENING POINT</li> <li>10. DUCTILITY OF BITUMEN</li> <li>11. FLASH AND FIRE POINT OF BITUMEN</li> <li>12. DESIGN OF BITUMINOUS MIX DESIGN OF BITUMINOUS MIX BY MARSHALL METHOD OF MIX DESIGN</li> </ol>
9	ENVIRONMENTAL ENGINEERING LAB	<ol style="list-style-type: none"> <li>1. TO ANALYSE THE PHYSICAL CHARACTERISTICS VIZ. COLOUR, TURBIDITY, AND CONDUCTIVITY OF A GIVEN WATER SAMPLE AND TO DETERMINE ITS SUITABILITY FOR DRINKING PURPOSES</li> <li>2. TO ANALYSE THE CHEMICAL CHARACTERISTICS OF A GIVEN WATER SAMPLE VIZ. PH, ACIDITY, ALKALINITY FOR ASSESSING ITS POTABILITY</li> <li>3. TO ANALYSE THE CHEMICAL CHARACTERISTICS OF A GIVEN WATER SAMPLE VIZ. CHLORIDES AND SULPHATES CONTENT TO ASSESS ITS SUITABILITY FOR DRINKING PURPOSES AND BUILDING CONSTRUCTION</li> <li>4. TO DETERMINE THE DISSOLVED OXYGEN CONTENT OF A GIVEN WATER SAMPLE FOR CHECKING ITS POTABILITY</li> <li>5. TO DETERMINE THE AVAILABLE CHLORINE IN A SAMPLE OF BLEACHING POWDER</li> <li>6. TO ANALYSE THE VARIOUS TYPES OF SOLIDS IN A GIVEN WATER SAMPLE</li> <li>7. TO DETERMINE THE BOD OF A GIVEN WASTEWATER SAMPLE</li> <li>8. TO DETERMINE THE COD OF A GIVEN WASTEWATER SAMPLE</li> <li>9. TO DETERMINE THE OPTIMUM DOSAGE OF ALUM USING JAR TEST</li> <li>10. TO DETERMINE THE NITRATES / PHOSPHATES IN A WATER SAMPLE</li> <li>11. TO DETERMINE THE IRON CONTENT OF A WATER SAMPLE</li> <li>12. TO DETERMINE THE MPN CONTENT IN A WATER SAMPLE AND ASSESS THE SUITABILITY FOR POTABILITY</li> </ol>

10	STRUCTURAL ENGINEERING DESIGN STUDIO	STAAD & STRAP: LINEAR STATIC ANALYSIS, DESIGN & DETAILING OF CONTINUOUS BEAMS, PORTAL FRAMES, TRUSS (2D AND 3D), MULTI-STOREYED BUILDING.
11	COMPUTER APPLICATION LAB	SAP 2000:- LINEAR STATIC ANALYSIS OF CONTINUOUS BEAMS, PORTAL FRAMES, TRUSS (2D AND 3D), MULTI-STOREYED BUILDING. ANSYS AND NISA:- LINEAR STATIC ANALYSIS OF CONTINUOUS BEAMS, PORTAL FRAMES, TRUSS (2D AND 3D), PLATES (PLANE STRESS AND PLANE STRAIN)

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1	Electrical and Electronics Workshop	<p>1a) Demonstrate the precautionary steps adopted in case of Electrical shocks. b)Identify different types of cables, wires, switches, fuses, fuse carriers, MCB, ELCB and MCCB with ratings.</p> <p>2.Wiring of Simple Light Circuit for Controlling Light/Fan Point(PVC Conduit wiring)                      3.Wiring of light circuit using Two way switches . (Staircase wiring)</p> <p>4.Wiring of Fluorescent lamps and Light sockets(6A) with a power circuit for controlling power device</p> <p>5.Wiring of power distribution arrangement using single phase MCB distribution board with ELCB, main switch and Energy meter.</p> <p>6 a)Identify different types of batteries with their specifications.    b)Demonstrate the Pipe and Plate Earthing Schemes using Charts/Site Visit.</p>
2	Electrical Machines Lab I	<p>1. OCC on a dc shunt generator, determination of critical resistance, critical speed, additional resistance required in the field circuit</p> <p>2. Load characteristics of DC Shunt generator</p> <p>3. Load characteristics of DC Compound generator</p> <p>4. Load test on DC Series motor</p> <p>5. Load test on DC Shunt motor</p> <p>6. Swinburne's Test on a DC Shunt Machine</p> <p>7.Hopkinson's test on a pair of DC machines</p> <p>8.Separation of losses in a DC shunt motor</p> <p>9. Load test on single phase transformer</p> <p>10. Sumpner's Test.</p> <p>11. OC and SC test on single phase transformer</p> <p>12.Separation of Constant losses of a Single Phase Transformer</p>



3	<p style="text-align: center;"><b>CIRCUITS AND MEASUREMENTS LAB</b></p>	<ol style="list-style-type: none"> <li>1. Verification of Superposition theorem and Thevenin's theorem.</li> <li>2. Determination of impedance, admittance and power factor in RLC series/ parallel circuits.</li> <li>3. 3-phase power measurement using one wattmeter and two-wattmeter methods, and determination of reactive/apparent power drawn.</li> <li>4. Resistance measurement using Kelvin's Double Bridge and Wheatstone's Bridge</li> <li>5. Extension of instrument range by using Instrument transformers(CT and PT)</li> <li>6. Calibration of ammeter, voltmeter, wattmeter using Potentiometers</li> <li>7. Calibration of 1-phase Energy meter at various power factors (minimum 4 conditions)</li> <li>8. Calibration of 3-phase Energy meter using standard wattmeter</li> <li>9. Determination of B-H curve</li> <li>10. Measurement of Self inductance, Mutual inductance and Coupling coefficient of a 1-phase transformer</li> <li>11. . Verification of loading effect in ammeters and voltmeters with current measurement using Clamp on meter..</li> <li style="text-align: right;">12. calibration of 1 phase energy meter by phantom loading at adifferent power factors</li> <li>13. Experiments/Simulation study: <ol style="list-style-type: none"> <li>(a) Measurement of energy using TOD meter</li> <li>(b) Measurement of electrical variables using DSO</li> </ol> </li> </ol>
4	<p style="text-align: center;"><b>CONTROL &amp; SIMULATION LAB</b></p>	<ol style="list-style-type: none"> <li>1.Predetermination and verification of frequency response characteristics of Lag networks.</li> <li>2.Predetermination and verification of frequency response characteristics of Lead networks.</li> <li>3.Study of various types of synchros (TX, TR &amp; TDX). Characteristics of transmitter, data transmission using TX-T R pair. Effect of TDX in data transmission.</li> <li>4.Transfer Function of AC servomotor</li> <li>5.MATLAB: Use of control system Tool box for the Time domain and frequency domain methods of system analysis and design</li> <li>Step and impulse response of open loop and closed loop systems(MATLAB)</li> <li>Step and frequency response of R-L-C circuit</li> <li>Lag compensator design using Bode plot with MATLAB control system Tool box</li> <li>Lead compensator design using Bode plot with MATLAB control system Tool box</li> <li>SIMULINK: Simulation and control of real time systems using SIMULINK</li> </ol>

5	Power Electronics Lab	<ol style="list-style-type: none"> <li>1. Static characteristics of SCR</li> <li>2. R and RC firing circuits</li> <li>3. UJT Trigger circuit with Single phase controlled Rectifier</li> <li>4. Line Synchronized Triggering Circuits</li> <li>5. AC Voltage Controller using TRIAC</li> <li>6. Single phase fully controlled SCR Bridge Circuit.</li> <li>7. Chopper Controlled DC motor</li> <li>8. Simulation of 1-phase fully-controlled and half –controlled rectifier fed separately excited DC motor.</li> <li>9. Design and simulation of buck, boost and buck-boost converters</li> <li>10. Simulation of Dual Converter – 4 quadrant operation – separately excited DC motor</li> <li>11. Speed control of DC motor Drive</li> </ol>
	MICROPROCESSOR & MICROCONTROLLER LAB	<p>Embedded Systems</p> <ol style="list-style-type: none"> <li>1. Introduction to 8085</li> <li>2. Data Transfer in Different Addressing modes</li> <li>3. Binary and BCD Arithmetic Operations</li> <li>4. Largest/Smallest Number of An Array</li> <li>5. Sorting an Array</li> <li>6. Code Conversion</li> <li>7. Introduction to 8051</li> <li>8. Square Wave Generation Using 8051</li> <li>9. LCD Display Interfacing With 8051</li> </ol>
6	POWER SYSTEMS LAB	<ol style="list-style-type: none"> <li>1. Y-Bus Formulation(Basic Programming):</li> <li>2. Load Flow Analysis –Gauss-Siedel Method</li> <li>3. Short Circuit Analysis – Symmetrical Faults and Unsymmetrical Faults</li> <li>4. Transient Stability Analysis</li> <li>5. Automatic Generation Control – Single Area, Two Area</li> <li>6. Reactive Power Control</li> <li>7. Plot the IV characteristics of a PV module and determine Maximum Power Point</li> <li>8. Testing of dielectric strength of solid insulating materials</li> <li>9. Testing of dielectric strength of air</li> <li>10. Testing of CT and PT</li> <li>11. Testing of transformer oil</li> <li>12. Power factor improvement</li> <li>13. Insulation Testing – LT &amp; HT Cable</li> <li>14. Earth Resistance</li> </ol>

7	Electrical Machines Lab II	<p>Regulation of alternator by direct loading</p> <p>Regulation of three phase alternator by emf and mmf methods</p> <p>Regulation of alternator by Potier and ASA methods</p> <p>Variation of starting torque with rotor resistance in slip-ring induction motors</p> <p>Load test on three phase squirrel cage induction motor</p> <p>Load test on three slip ring induction motor</p> <p>No load and block rotor test on three phase induction motor</p> <p>Performance characteristics of pole changing induction motor</p> <p>V curve of a synchronous motor</p> <p>Performance characteristics of induction generator</p> <p>Equivalent circuit of single phase induction motor</p>
8	ANALOG CIRCUITS LAB	<ol style="list-style-type: none"> <li>1) Measurement of current, voltage, frequency and phase shift of signal in a RC network using oscilloscope.</li> <li>2. Clipping circuits using diodes.</li> <li>3. Clamping circuits using diodes.</li> <li>4. RC coupled amplifier using BJT in CE configuration-Measurement of gain, BW and plotting of frequency response.</li> <li>5. Op-amp circuits – Design and set up of inverting and non-inverting amplifier, scale changer, adder, integrator, and differentiator.</li> <li>6. Op-amps circuits – Scale changer, adder, integrator, and differentiator.</li> <li>7. Precision rectifier using Op-amps.</li> <li>8. Wein's Bridge oscillator using Op-amps.</li> <li>9. Waveform generation– Square, triangular and saw tooth waveform generation using OPAMPs.</li> <li>10. Basic comparator and Schmitt trigger circuits using Op-amp (Use comparator ICs such as LM311).</li> <li>11. Astable and Monostable circuit using 555IC.</li> <li>12. RC phase shift oscillator using Op-amp.</li> <li>13. Introduction to circuit simulation using any circuit simulation software.(PSpice)</li> </ol>

9	Digital Electronics Lab	<ol style="list-style-type: none"> <li>1. Familiarisation of Logic Gates, Identification of typical logic ICs, Interpreting IC datasheets</li> <li>2. Verification &amp; Realisation of De Morgan's theorem.</li> <li>3. Realisation of SOP &amp; POS functions after K-map reduction.</li> <li>4. Half adder &amp; Full adder using gates.</li> <li>5. 4-bit Adder/ Subtractor &amp; BCD adder using IC 7483.</li> <li>6. Realisation of 2-bit comparator using gates and study of four-bit comparator IC 7485.</li> <li>7. BCD to decimal decoder and BCD to 7-segment decoder &amp; display.</li> <li>8. Study of multiplexer IC and realization of combinational circuits using multiplexers.</li> <li>9. Realization of RS, T, D &amp; JK flip flops using gates.</li> <li>10. Study of flip flop ICs (7474 &amp; 7476).</li> <li>11. Realisation of ripple up and down counters and modulo-N counter using flip-flops.</li> <li>12. Study of counter ICs (7490, 7493).</li> <li>13. Design of synchronous up, down &amp; modulo-N counters.</li> <li>14. Realization of 4-bit serial IN serial OUT registers using flip flops.</li> <li>15. Study of shift register IC 7495, ring counter and Johnsons counter.</li> <li>16. VHDL implementation of full adder, 4 bit magnitude comparator</li> </ol>
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# LIST OF EXPERIMENTS-ECE

SL NO	NAME OF LAB/WORKSHOP	LIST OF EXPERIMENTS
1	S12: ELECTRICAL AND ELECTRONICS WORKSHOP (2019 scheme)	1.Familiarization of Electronic Components 2.Drawing of Electronic Circuit Diagrams Using BIS/IEEE Symbols and Introduction to EDA Tools. Interpret datasheets of discrete components and IC's 3.Familiarization of Testing Instruments and Commonly Used Tools 4.Testing of Electronic Components Using Multi-meters 5.Interconnection Methods and Soldering Practice using General Purpose PCB 6.Printed Circuit Boards and Processing Methods 7.Assembling of Electronic Circuits on General Purpose PCB
2	S3: SCIENTIFIC COMPUTING LAB (2019 scheme)	1. Familiarization of the Computing Tool 2. Familiarization of Scientific Computing 3. Realization of Arrays and Matrices 4. Numerical Differentiation and Integration 5. Solution of Ordinary Differential Equations 6. Simple Data Visualization 7. Simple Data Analysis with Spreadsheets 8. Convergence of Fourier Series 9: Coin Toss and the Level Crossing Problem
3	S3: LOGIC DESIGN LAB (2019 scheme)	PART A 1.Realization of functions using basic and universal gates. 2.Design and realization of half/full adder and subtractor using basic and universal gates. 3.4 bit adder/subtractor and BCD adder using 7483. 4.Asynchronous counter: 3 bit up/down counter. 5.Ring and Johnson counter. PART B 1.Realization of logic gates and familiarization of FPGAs. 2.Develop a verilog module for adders in 3 modelling styles. 3.Develop a verilog module for Mux and Demux. 4.Develop a verilog module for binary decade/Ring/Johnson counter. 5.Synchronous and Asynchronous counters in FPGA.

4	S4:ANALOG CIRCUITS AND SIMULATION LAB (2019 scheme)	<p>1. RC integrating and differentiating circuits (Transient analysis with different inputs and frequency response)</p> <p>2.Clipping and clamping circuits (Transients and transfer characteristics)</p> <p>3.RC coupled CE amplifier - frequency response characteristics</p> <p>4.Feedback amplifiers (current series, voltage series) - gain and frequency response</p> <p>5.Low frequency oscillators –RC phase shift or Wien bridge</p> <p>6.Power amplifiers (transformer less) - Class B and Class AB</p> <p>7.Transistor series voltage regulator (load and line regulation)</p> <p><b>SIMULATION</b></p> <p>1.RC integrating and differentiating circuits (Transient analysis with different inputs and frequency response)</p> <p>2.Clipping and clamping circuits (Transients and transfer characteristics)</p> <p>3.RC coupled CE amplifier - frequency response characteristics</p> <p>4.Feedback amplifiers (current series, voltage series) - gain and frequency response</p> <p>5.Low frequency oscillators –RC phase shift or Wien bridge</p> <p>6.Power amplifiers (transformer less) - Class B and Class AB</p> <p>7.Transistor series voltage regulator (load and line regulation)</p>
5	S4:MICROCONTROLLER LAB (2019 scheme)	<p>PART –A (At least 6 experiments are mandatory)</p> <p>These experiments shall be performed using 8051 trainer kit. The programs shall be written either in embedded C or in assembly language</p> <ol style="list-style-type: none"> <li>1. Data transfer/exchange between specified memory locations.</li> <li>2. Largest/smallest from a series.</li> <li>3. Sorting (Ascending/Descending) of data.</li> <li>4. Addition / subtraction / multiplication / division of 8/16 bit data.</li> <li>5. Sum of a series of 8 bit data.</li> <li>6. Square / cube / square root of 8 bit data.</li> </ol> <p>PART –B (At least 4 experiments are mandatory)</p> <p>Interfacing experiments shall be done using modern microcontrollers such as 8051 or ARM. The interfacing modules may be developed using Embedded C.</p> <ol style="list-style-type: none"> <li>1. Time delay generation and relay interface.</li> <li>2. Display (LED/Seven segments/LCD) and keyboard interface.</li> <li>3. ADC interface.</li> </ol>

6	S5:ANALOG INTEGRATED CIRCUITS & SIMULATION LAB (2019 SCHEME)	<p>I. Fundamentals of operational amplifiers and basic circuits</p> <ol style="list-style-type: none"> <li>1. Familiarization of Operational amplifiers - Inverting and Non inverting amplifiers, frequency response, Adder, Integrator, Comparators.</li> <li>2. Measurement of Op-Amp parameters.</li> <li>3. Schmitt trigger circuit using Op-Amps.</li> <li>4. Astable and Monostable multivibrator using Op-Amps</li> <li>5. Waveform generators using Op-Amps - Triangular and saw tooth</li> <li>6. RC Phase shift Oscillator.</li> <li>7. Precision rectifiers using Op-Amp</li> </ol> <p>II. Application circuits of 555 Timer/565 PLL/ Regulator(IC 723) ICs</p> <ol style="list-style-type: none"> <li>1. Astable and Monostable multivibrator using Timer IC NE555</li> <li>2. A/D converters- counter ramp and flash type.</li> <li>3. D/A Converters - R-2R ladder circuit</li> <li>4. Study of PLL IC: free running frequency lock range capture range</li> </ol> <p>III. Simulation experiments using SPICE</p> <ol style="list-style-type: none"> <li>1. Astable and Monostable multivibrator using Op-Amps</li> <li>2. Waveform generators using Op-Amps - Triangular and saw tooth</li> <li>3. RC Phase shift Oscillator.</li> <li>4. A/D converters- counter ramp and flash type.</li> </ol>
7	S5:DIGITAL SIGNAL PROCESSING LAB (2019 SCHEME)	<p>Simulation of Signals Verification of the properties of DFT Familiarisation of DSP Hardware Linear Convolution FFT of Signals IFFT with FFT FIR Low Pass Filter Overlap Save Block Reduction Overlap Add Block Reduction</p>
8	S6:ECL 332 COMMUNICATION LAB (2019 scheme)	<p>Part A Design and setup simple prototype circuits with the help of available ICs.</p> <ol style="list-style-type: none"> <li>1.FM generation and demodulation using PLL</li> <li>2.Generation and detection of BPSK</li> </ol> <p>Part B</p> <ol style="list-style-type: none"> <li>1. Performance of Waveform Coding Using PCM</li> <li>2.Pulse shaping and matched filtering</li> <li>3.Eye diagram</li> <li>4.Error performance of BPSK</li> <li>5.Error performance of QPSK</li> </ol> <p>Part C Emulate communication systems with the help of software-defined-radio hardware and necessary control software.</p> <ol style="list-style-type: none"> <li>1.Familiarization with software designed radio</li> <li>2.FM reception</li> </ol>
9	S6:MINI PROJECT (2019)	Design and develop a moderately complex electronic system with practical applications.

10	S2:C PROGRAMMING LAB (2019 scheme)	<p>Familiarization of hardware components of a computer  Familiarization of Linux environment- How to Do programming in C with Linux  Familiarization of console I/O and operators in C  1.Read 3 integer values and find the largest among them.  2.Read a Natural Number and check whether the number is prime or no  3.Read a Natural Number and check whether the number is Armstrong or not  4.Read n integers, store them in an array and find their sum and average  5.Read n integers, store them in an array and search for an element in the array using an algorithm for Linear Search  6.Read n integers, store them in an array and sort the elements in the array using Bubble Sort algorithm  7.Read a string (word), store it in an array and check whether it is a palindrome word or not.  8.Read two strings (each one ending with a \$ symbol), store them in arrays and concatenate them without using library functions.  9.Read a string (ending with a \$ symbol), store it in an array and count the number of vowels, consonants and spaces in it.  10.Read two input each representing the distances between two points in the Euclidean space, store these in structure variables and add the two distance values.  11.Using structure, read and print data of n employees (Name, Employee Id and Salary)  12.Declare a union containing 5 string variables (Name, House Name, City Name, State and Pin code) each with a length of C_SIZE (user</p>
11	S4 CSE: DIGITAL LAB (2019 SCHEME)	<p><b>PART A</b>  1.Realization of functions using basic and universal gates (SOP and POS forms).  2.Design and realization of half adder, full adder, half subtractor and full subtractor using: a) basic gates (b) universal gates.  3.Code converters: Design and implement BCD to Excess 3 and Binary to Gray code converters.  4.Design and implement 4 bit adder/subtractor circuit and BCD adder using IC7483.  5.Asynchronous Counter: Design and implement 3 bit up/down counter.  6.Synchronous Counter: Realization of 4-bit up/down counter.  7.Realization of Shift Register (Serial input left/right shift register), Ring counter and Johnson Counter using flipflops.  8.Realization of Multiplexers and De-multiplexers using gates.  <b>PART B</b>  1.Realization of Logic Gates and Familiarization of Verilog.  (a) Familiarization of the basic syntax of Verilog.  (b) Development of Verilog modules for basic gates and to verify truth tables.  (c) Design and simulate the HDL code to realize three and four variable Boolean functions.  2. Half adder and full adder.  (a) Development of Verilog modules for half adder in 3 modeling styles (dataflow/ structural/behavioural).  (b) Development of Verilog modules for full adder in structural modeling</p>



12	<p style="text-align: center;">S7: ELECTROMAGNETICS LAB (2019 scheme)</p>	<p>Microwave Experiments:</p> <ol style="list-style-type: none"> <li>1. Gunn Diode Characteristics</li> <li>2. VSWR and Frequency measurement</li> <li>3. Verification of wavelength relationship in a rectangular waveguide</li> <li>4. Reflex Klystron mode characteristics</li> <li>5. Directional coupler characteristics</li> <li>6. Impedance Measurement</li> </ol> <p>Optical Experiments</p> <ol style="list-style-type: none"> <li>1. Setting up a fiber optic analog link</li> <li>2. Setting up a fiber optic digital link</li> <li>3. Study of numerical aperture of optical fiber</li> <li>4. Study of losses in optical fiber</li> <li>5. VI characteristics of LED</li> <li>6. VI characteristics of laser</li> <li>7. PI characteristics of LED</li> </ol>
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# LIST OF EXPERIMENTS-CSE

SL NO	NAME OF LAB/WORKSHOP	LIST OF EXPERIMENTS
1	Data Structure Lab	<p>Write a program using linked lists to simulate Memory Allocation and Garbage Collection.</p> <p style="text-align: right;">Write a</p> <p>time/space efficient program to convert an arithmetic expression from one notation to another.</p> <p style="text-align: right;">Examine a given Data</p>
2	Operating System Lab	<p>Implement Process Creation and Inter Process Communication in Operating Systems.</p> <p style="text-align: right;">Illustrate the</p> <p>performance of First In First Out, Least Recently Used and Least Frequently Used Page Replacement Algorithms. Implement modules for Deadlock Detection and Deadlock Avoidance in Operating Systems.</p> <p>Implement modules for Storage Management and Disk Scheduling in Operating Systems.</p>
3	Database Management System lab	<p>Creation of a database using DDL commands and writes DQL queries to retrieve information from the database. Performing DML commands like Insertion, Deletion, Modifying, Altering, and Updating records based on conditions. Practice of SQL TCL commands like Rollback, Commit, Savepoint.</p> <p>Implementation of set operators, nested queries and Join queries. Implementation of various control structures using PL/SQL.</p> <p>Creation of Procedures and Functions.</p> <p style="text-align: right;">Creation of Packages.</p> <p style="text-align: center;">Creation of database Triggers and Cursors.</p> <p style="text-align: center;">Mini project (Application Development using Oracle/ MySQL using Database connectivity)</p>

4	System Software and Microprocessor Lab	<p>Simulate CPU scheduling algorithms. Implement Banker's algorithm for deadlock avoidance. Implement pass one and pass two of two pass assembler. Implement two pass macro processor. Implement an absolute loader. Implement a relocating loader. Implementation of simple decimal arithmetic and bit manipulation operations.</p> <p>Programming exercises using stack and subroutines. Implementation of String manipulations. Interfacing with stepper motor - Rotate through any given sequence. Interfacing with 8255 (mode0 and mode1 only). Interfacing with Digital-to-Analog Converter. Familiarization of 8051 trainer kit by executing simple Assembly Language programs such as decimal arithmetic and bit manipulation.</p>
5	Networking Lab	<p>Implement Client-Server communication using Socket Programming and TCP as transport layer protocol. Implement Client-Server communication using Socket Programming and UDP as transport layer protocol. Implement a multi user chat server using TCP as transport layer protocol.</p> <p>Implement Concurrent Time Server application using UDP to execute the program at remoteserver. Client sends a time request to the server, server sends its system time back to the client. Client displays the result.</p> <p>Develop concurrent file server which will provide the file requested by client if it exists. If not server sends appropriate message to the client. Server should also send its process ID (PID) to clients for display along with file or the message.</p>

## LIST OF EXPERIMENTS-SCIENCE

SL NO	NAME OF LAB/WORKSHOP	LIST OF EXPERIMENTS
1	Engineering chemistry lab CYL 120	Estimation of total Hardness of water- EDTA method
2	Engineering chemistry lab CYL 120	Potentiometric Titration - Redox reaction
3	Engineering chemistry lab CYL 120	Determination of cell constant and conductance of solutions
4	Engineering chemistry lab CYL 120	Calibration of p H meter and Determination of pH of a solution
5	Engineering chemistry lab CYL 120	Estimation of chloride in water
6	Engineering chemistry lab CYL 120	Determination of wavelength of absorption maximum and Colorimetric Estimation of iron in solution
7	Engineering chemistry lab CYL 120	Synthesis of polymers a) Urea-formaldehyde resin b) Phenol-formaldehyde resin
8	Engineering chemistry lab CYL 120	Estimation of Dissolved Oxygen by Winkler's method
1	Engineering Physics lab PHL 120	CRO-Measurement of frequency and amplitude of wave forms
2	Engineering Physics lab PHL 120	Melde's string apparatus-Measurement of frequency in the transverse and longitudinal mode
3	Engineering Physics lab PHL120	Wave length measurement of a monochromatic source of light using Newton's Rings
4	Engineering Physics lab PHL120	Determination of diameter of a thin wire using air wedge method
5	Engineering Physics lab PHL120	To measure the wavelength using a millimeter scale as a grating
6	Engineering Physics lab PHL 120	Measurement of wavelength of a source of light using grating
7	Engineering Physics lab PHL 120	Calculate the numerical aperture and study the losses that occur in optical fiber cable
8	Engineering Physics lab PHL 120	I-V characteristics of solar cell