

**Course: B.Tech(KTU) Academic Year: 2020-2024 Branch: Electronics and Communication Engineering Semester: S6 Division: A**

	1	2	3	4	LUNCH	5	6	7	
Monday	Digital Image Processing Jeena 9:00 AM-9:50 AM	VLSI CIRCUIT DESIGN Mercy 9:50 AM-10:40 AM	ELECTROMAGNETICS Shainy 10:50 AM-11:40 AM	Digital Image Processing Jeena 11:40 AM-12:30 PM	LUNCH BREAK	ELECTROMAGNETICS Shainy 1:30 PM-2:20 PM	VLSI CIRCUIT DESIGN Mercy 2:20 PM-3:10 PM	INFORMATION THEORY AND CODING Soumya 3:20 PM-4:10 PM	
Tuesday	INFORMATION THEORY AND CODING Soumya 9:00 AM-9:50 AM	ELECTROMAGNETICS Shainy 9:50 AM-10:40 AM	INFORMATION THEORY AND CODING Soumya 10:50 AM-11:40 AM	VLSI CIRCUIT DESIGN Mercy 11:40 AM-12:30 PM		COMMUNICATION LAB Mercy/Shainy 1:30 PM-4:10 PM			
Wednesday	VLSI CIRCUIT DESIGN Mercy 9:00 AM-9:50 AM	Digital Image Processing Jeena 9:50 AM-10:40 AM	ELECTROMAGNETICS Shainy 10:50 AM-11:40 AM	INFORMATION THEORY AND CODING Soumya 11:40 AM-12:30 PM		COMMUNICATION LAB Mercy/Jeena 1:30 PM-4:10 PM			
Thursday	ELECTROMAGNETICS Shainy 9:00 AM-9:50 AM	INFORMATION THEORY AND CODING Soumya 9:50 AM-10:40 AM	Digital Image Processing Jeena 10:50 AM-11:40 AM	MANAGEMENT FOR ENGINEERS Jikku 11:40 AM-12:30 PM		VLSI CIRCUIT DESIGN Mercy 1:30 PM-2:20 PM	ELECTROMAGNETICS Shainy 2:20 PM-3:10 PM	MANAGEMENT FOR ENGINEERS Jikku 3:20 PM-4:10 PM	
Friday	VLSI CIRCUIT DESIGN Mercy 9:00 AM-9:45 AM	INFORMATION THEORY AND CODING Soumya 9:45 AM-10:30 AM	Digital Image Processing Jeena 10:30 AM-11:15 AM	MANAGEMENT FOR ENGINEERS Jikku 11:15 AM-12:00 PM			COMPREHENSIVE COURSE WORK Jeena 2:30 PM-4:00 PM		
Saturday									

\* Time table from 10/04/2023 to 14/04/2023