

Course: B.Tech(KTU) Academic Year: 2021-2025 Branch: Electronics and Communication Engineering Semester: S4 Division: A

	1	2	3	4	LUNCH	5	6	7
Monday	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS Elsa 9:00 AM-9:50 AM	DESIGN AND ENGINEERING Jimson 9:50 AM-10:40 AM	COMPUTER ARCHITECTURE AND MICROCONTROLLERS Dr. Sajin 10:50 AM-11:40 AM	Nanoelectronics Hyma 11:40 AM-12:30 PM	LUNCH BREAK	SIGNALS AND SYSTEMS Sherin 1:30 PM-2:20 PM	COMPUTER ARCHITECTURE AND MICROCONTROLLERS Dr. Sajin 2:20 PM-3:10 PM	ANALOG CIRCUITS Shainy 3:20 PM-4:10 PM
Tuesday	COMPUTER ARCHITECTURE AND MICROCONTROLLERS Dr. Sajin 9:00 AM-9:50 AM	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS Elsa 9:50 AM-10:40 AM	CONSTITUTION OF INDIA Fr. Dannis 10:50 AM-11:40 AM	ANALOG CIRCUITS Shainy 11:40 AM-12:30 PM		SIGNALS AND SYSTEMS Sherin 1:30 PM-2:20 PM	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS Elsa 2:20 PM-3:10 PM	
Wednesday	ANALOG CIRCUITS Shainy 9:00 AM-9:50 AM	SIGNALS AND SYSTEMS Sherin 9:50 AM-10:40 AM	Nanoelectronics Hyma 10:50 AM-11:40 AM	COMPUTER ARCHITECTURE AND MICROCONTROLLERS Dr. Sajin 11:40 AM-12:30 PM		ANALOG CIRCUITS AND SIMULATION LAB Sherin/Shainy 1:30 PM-4:10 PM		
Thursday	SIGNALS AND SYSTEMS Sherin 9:00 AM-9:50 AM	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS Elsa 9:50 AM-10:40 AM	ANALOG CIRCUITS Shainy 10:50 AM-11:40 AM	COMPUTER ARCHITECTURE AND MICROCONTROLLERS Dr. Sajin 11:40 AM-12:30 PM		MICROCONTROLLER LAB Jeena/Dr. Sajin 1:30 PM-4:10 PM		
Friday	ANALOG CIRCUITS Shainy 9:00 AM-9:45 AM	Nanoelectronics Hyma 9:45 AM-10:30 AM	SIGNALS AND SYSTEMS Sherin 10:30 AM-11:15 AM	CONSTITUTION OF INDIA Fr. Dannis 11:15 AM-12:00 PM		Nanoelectronics Hyma 2:00 PM-2:30 PM	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS Elsa 2:30 PM-3:15 PM	DESIGN AND ENGINEERING Jimson 3:15 PM-4:00 PM
Saturday								

* Time table from 10/04/2023 to 14/04/2023